

```
graph RL; Keys[Keys] --> GPIO[Generic Port I/O];
```

Generic Port I/O

The diagram consists of two rectangular boxes connected by a horizontal arrow. The box on the left is white with a black border and contains the text 'Generic Port I/O'. The box on the right is gray with a black border and contains the text 'Keys'. A blue arrow points from the 'Keys' box to the 'Generic Port I/O' box.

Keys